

Form PTO-1449 (modified)

Atty. Docket No.

Serial No.

11762.0266.NPUS00

08/530,661

**List of Patents and Publications for Applicant's****INFORMATION DISCLOSURE STATEMENT**

(Use several sheets if necessary)

**Applicants****Brent Keeth & Pierre C. Fazan****Filing Date:****Group:**

Sept. 20, 1995

2814

**U.S. Patent Documents**

<b>Exam. Init.</b>	<b>Ref. Des.</b>	<b>Document Number</b>	<b>Date</b>	<b>Name</b>	<b>Class</b>	<b>Sub Class</b>	<b>Filing Date of App.</b>
	A1	6,429,529	08/06/2002	Keeth	257	776	03/23/2000
	A2	6,392,303	05/21/2002	Keeth	257	776	04/05/2001
	A3	6,288,421	09/11/2001	Keeth et al.	257	296	09/15/1997
	A4	6,243,311	06/05/2001	Keeth	365	206	02/18/2000
	A5	6,222,275	04/24/2001	Keeth	257	776	02/18/2000
	A6	6,084,307	07/04/2000	Keeth	257	776	12/15/1998
	A7	6,043,562	03/28/2000	Keeth	257	776	08/22/1996
	A8	5,866,453	02/02/1999	Prall et al.	438	253	09/14/1995
	A9	5,864,181	01/26/1999	Keeth	257	776	10/15/1997
	A10	5,770,500	06/23/1998	Batra et al.	438	255	11/15/1996
	A11	5,629,230	05/13/1997	Fazan et al.	438	446	08/01/1995
	A12	5,555,212	09/10/1996	Toshiaki et al.	365	200	09/19/1994
	A13	5,407,534	04/18/1995	Thakur	156	662	12/10/1993
	A14	5,128,737	07/07/1992	van der Have	357	40	03/02/1990

**Other Art (Including Author, Title, Date Pertinent Pages, Etc.)**

<b>Exam. Init.</b>	<b>Ref. Des.</b>	<b>Citation</b>
	C1	Eimori et. al., "A Newly Designed Planar Stacked Capacitor Cell with High Dielectric Constant Film for 256Mbit DRAM," IEDM 1993 pp. 26.3.1-4.

**EXAMINER:****DATE CONSIDERED:**

3 Mar 03

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INFORMATION DISCLOSURE STATEMENT -- PTO-1449 (MODIFIED)